

Figure 3.



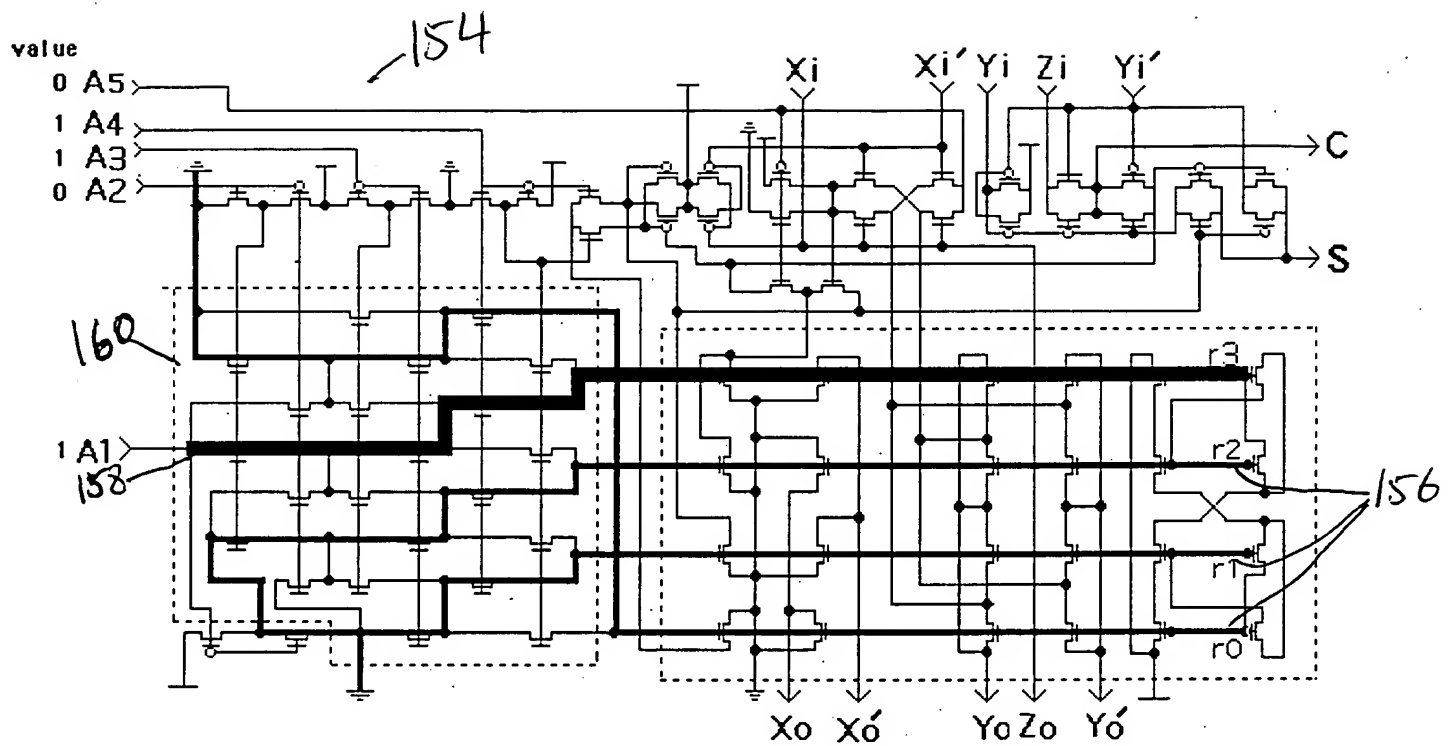


Figure 5

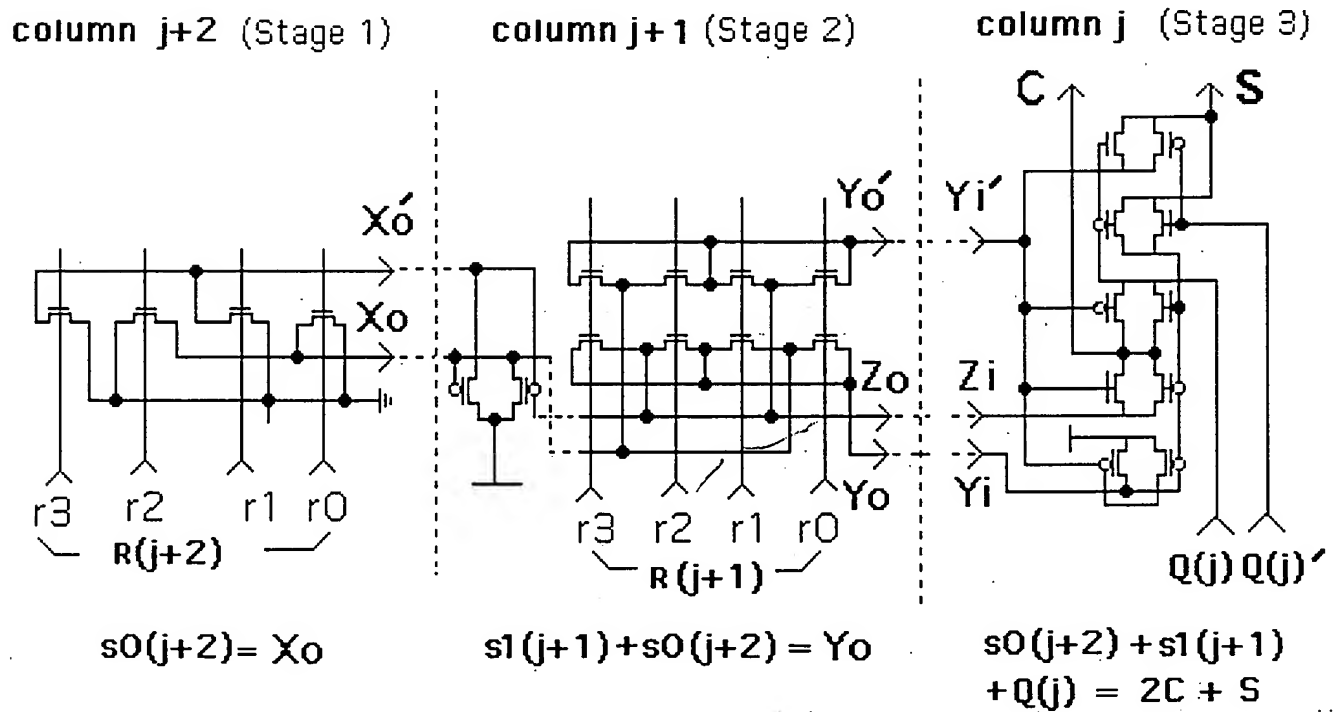


Figure 6

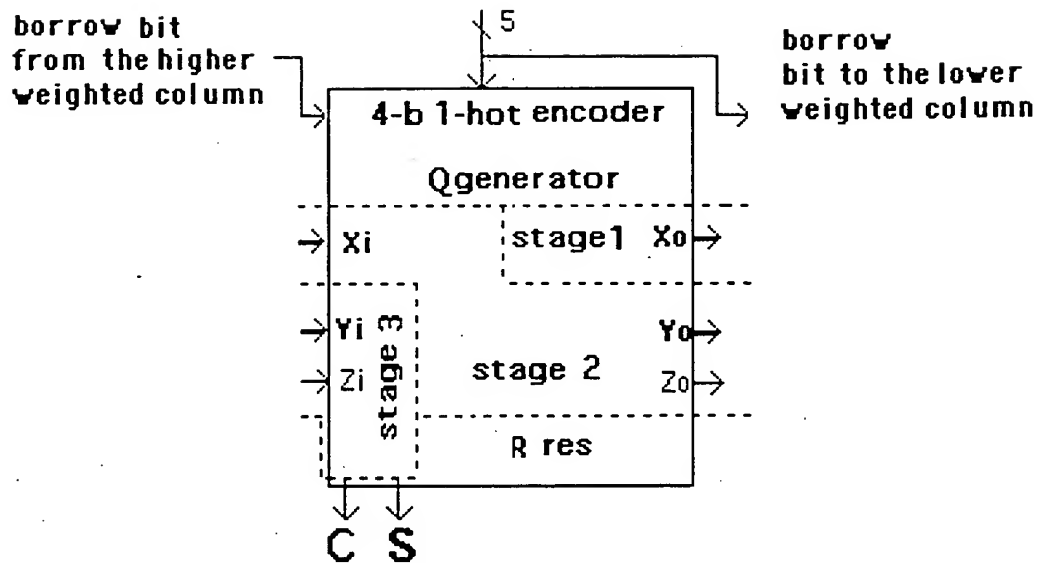


Figure 7.

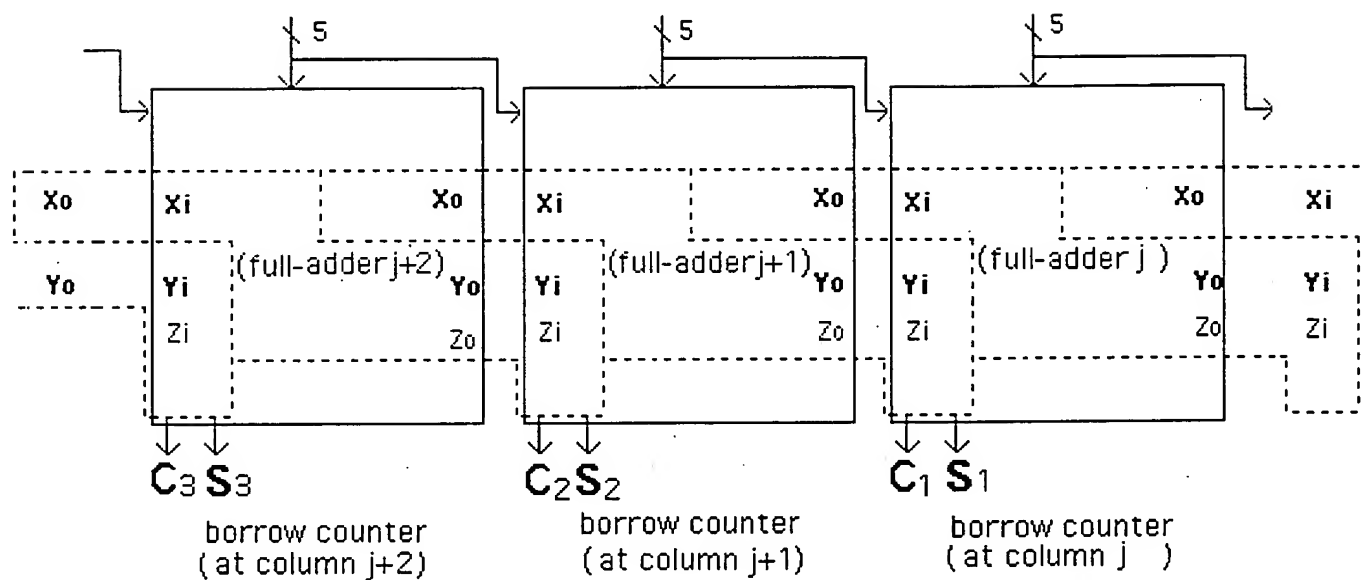


Figure 8.

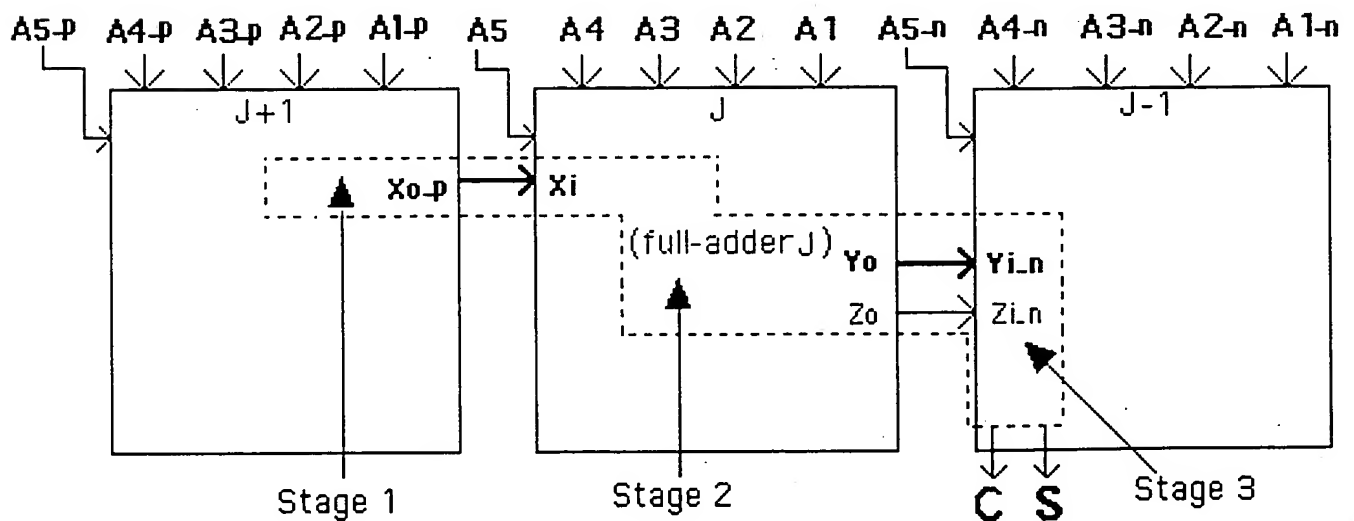


Figure 9.

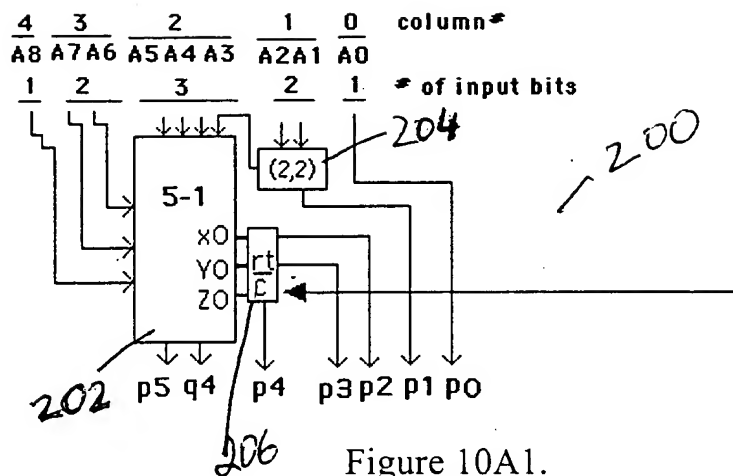


Figure 10A1.

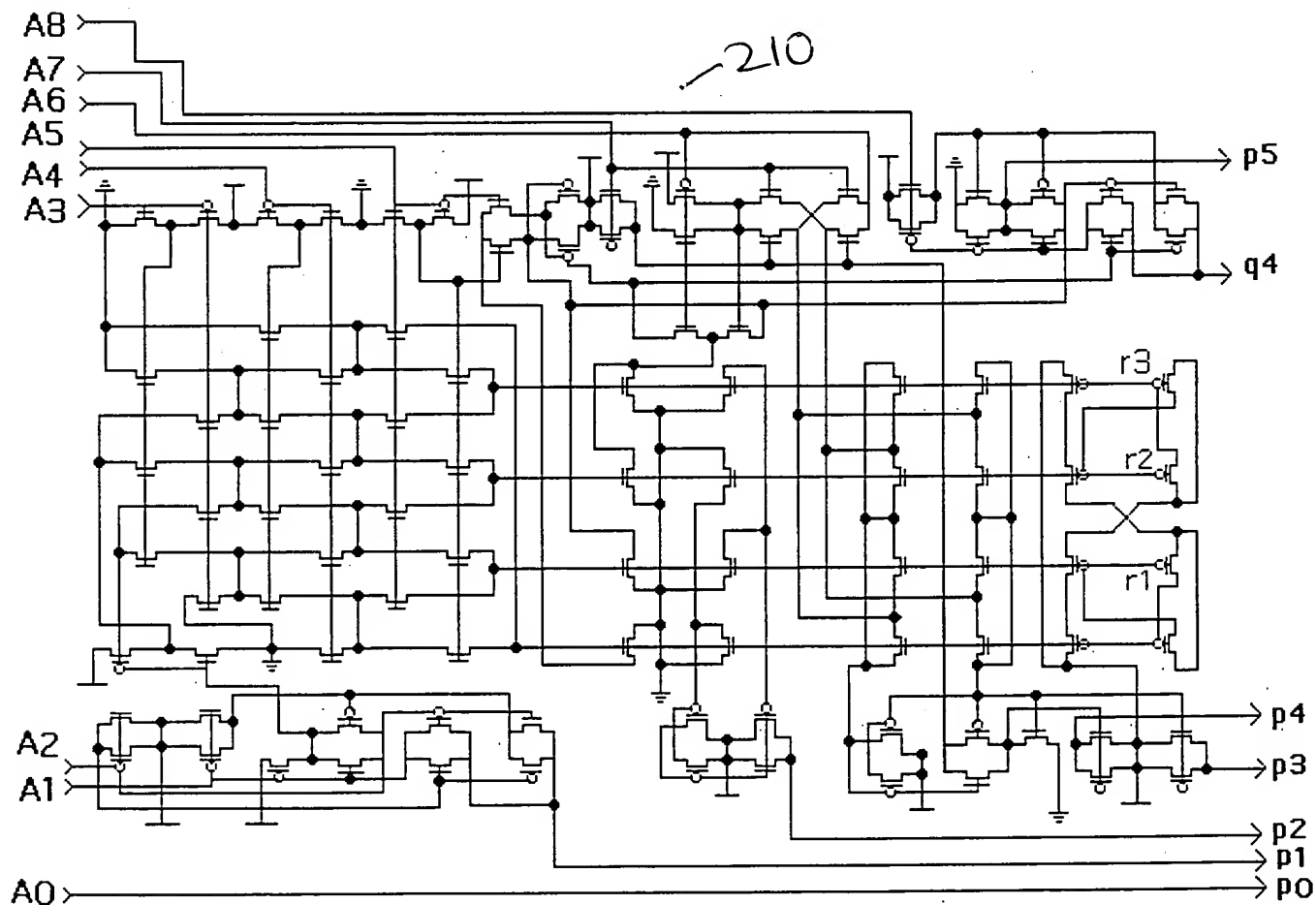
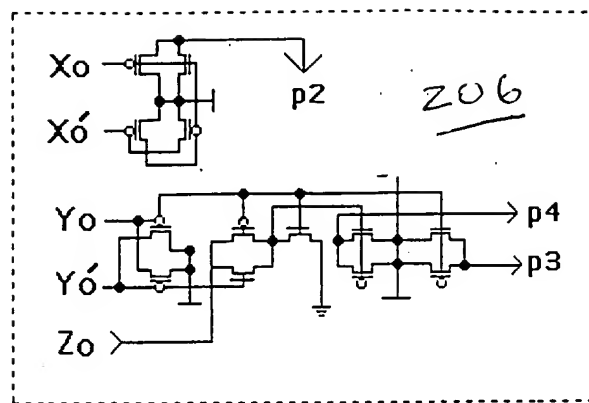


Figure 10A2.

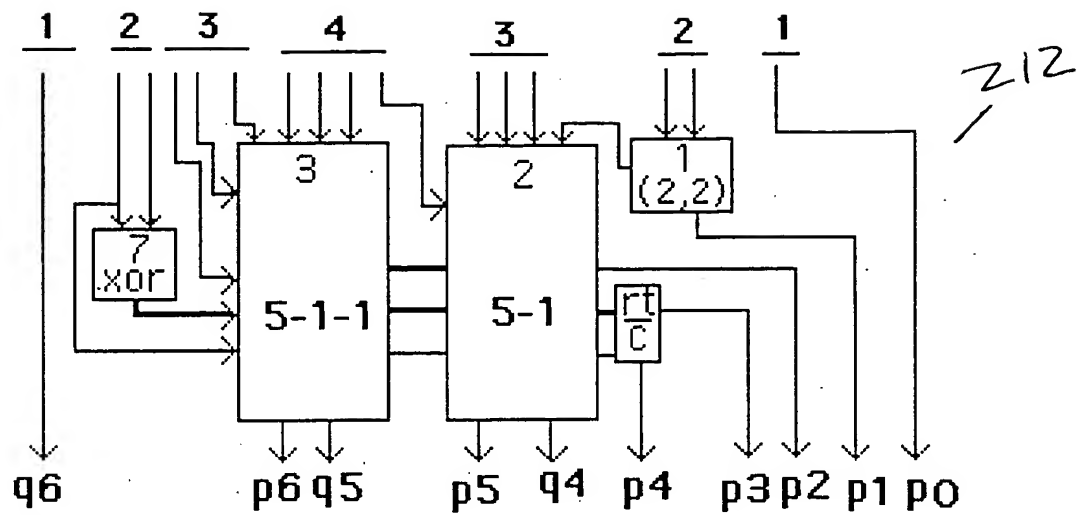


Figure 10A3.

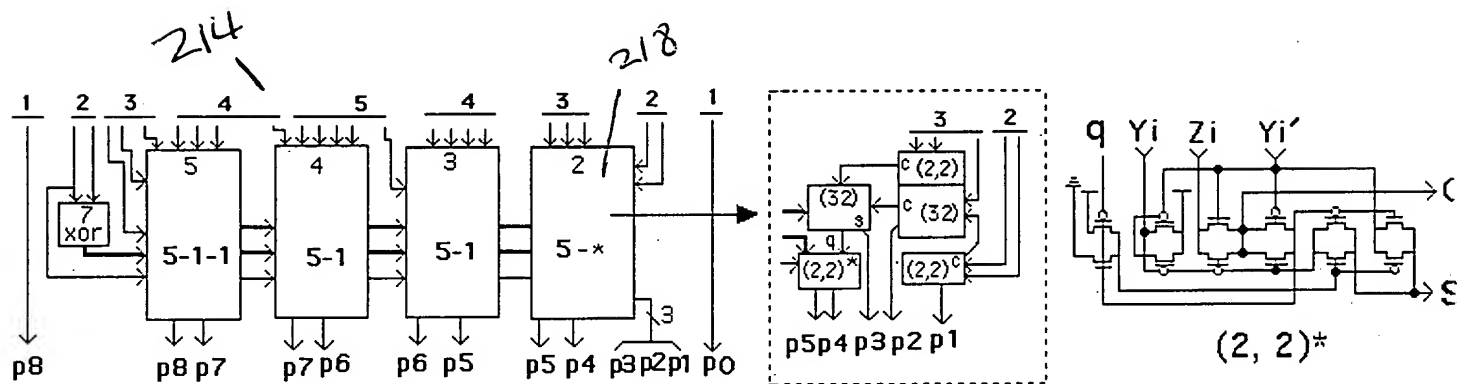


Figure 10A4. The 5x5a (virtual) multiplier.

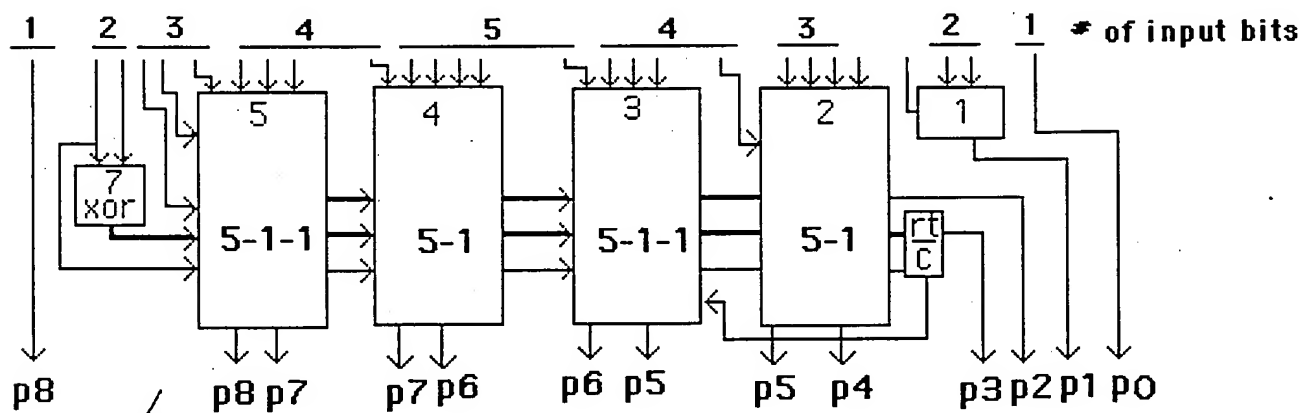


Figure 10A5.





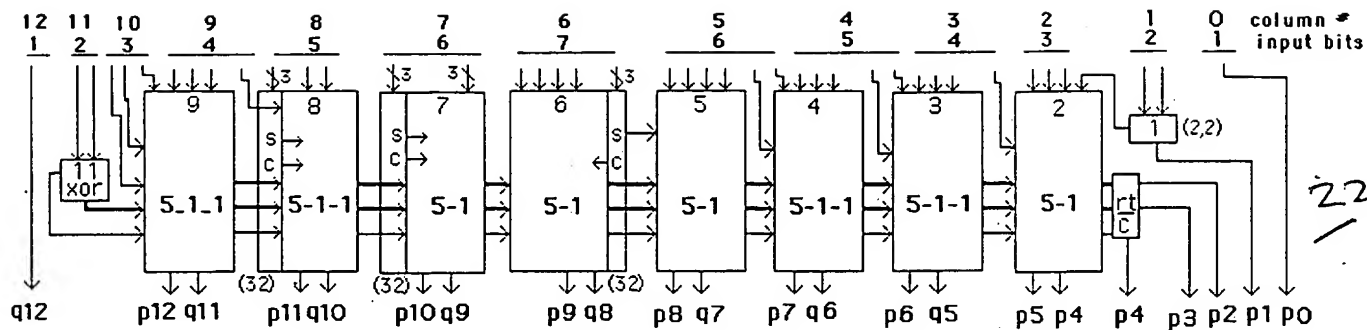


Figure 10A9.

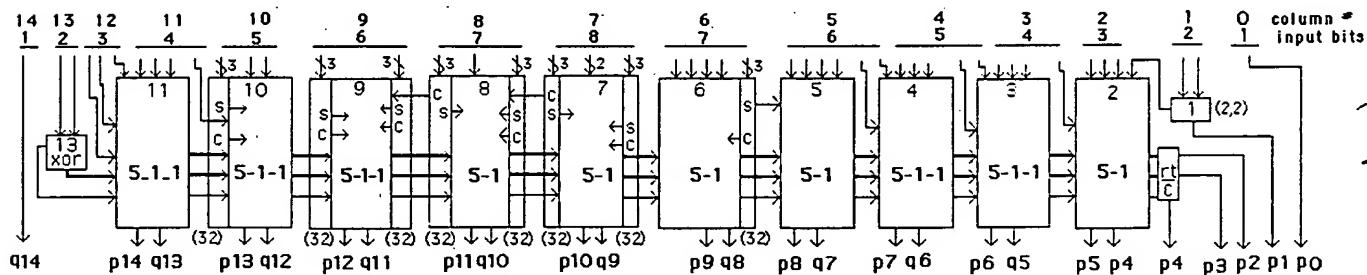


Figure 10A10.

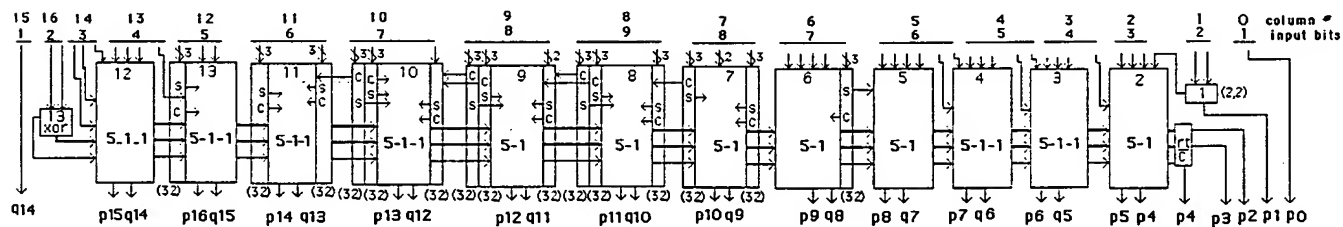


Figure 10A11.

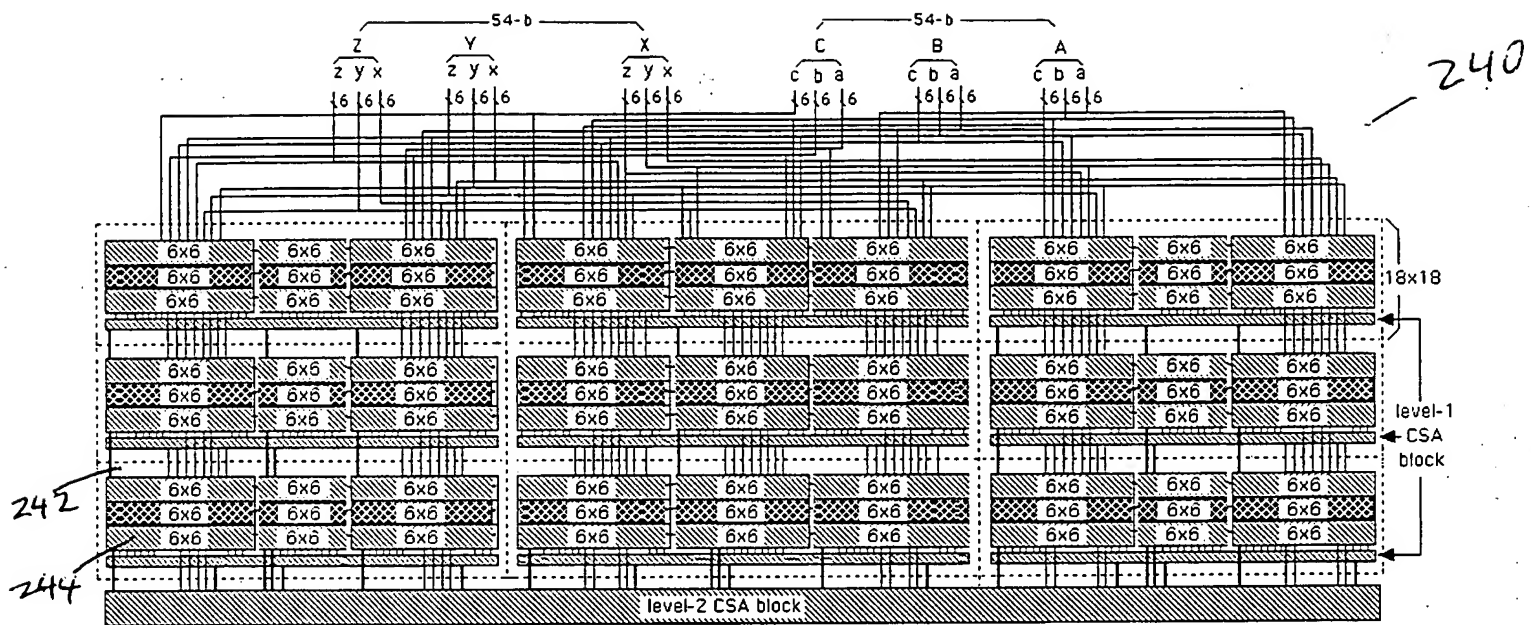


Figure 10B1.

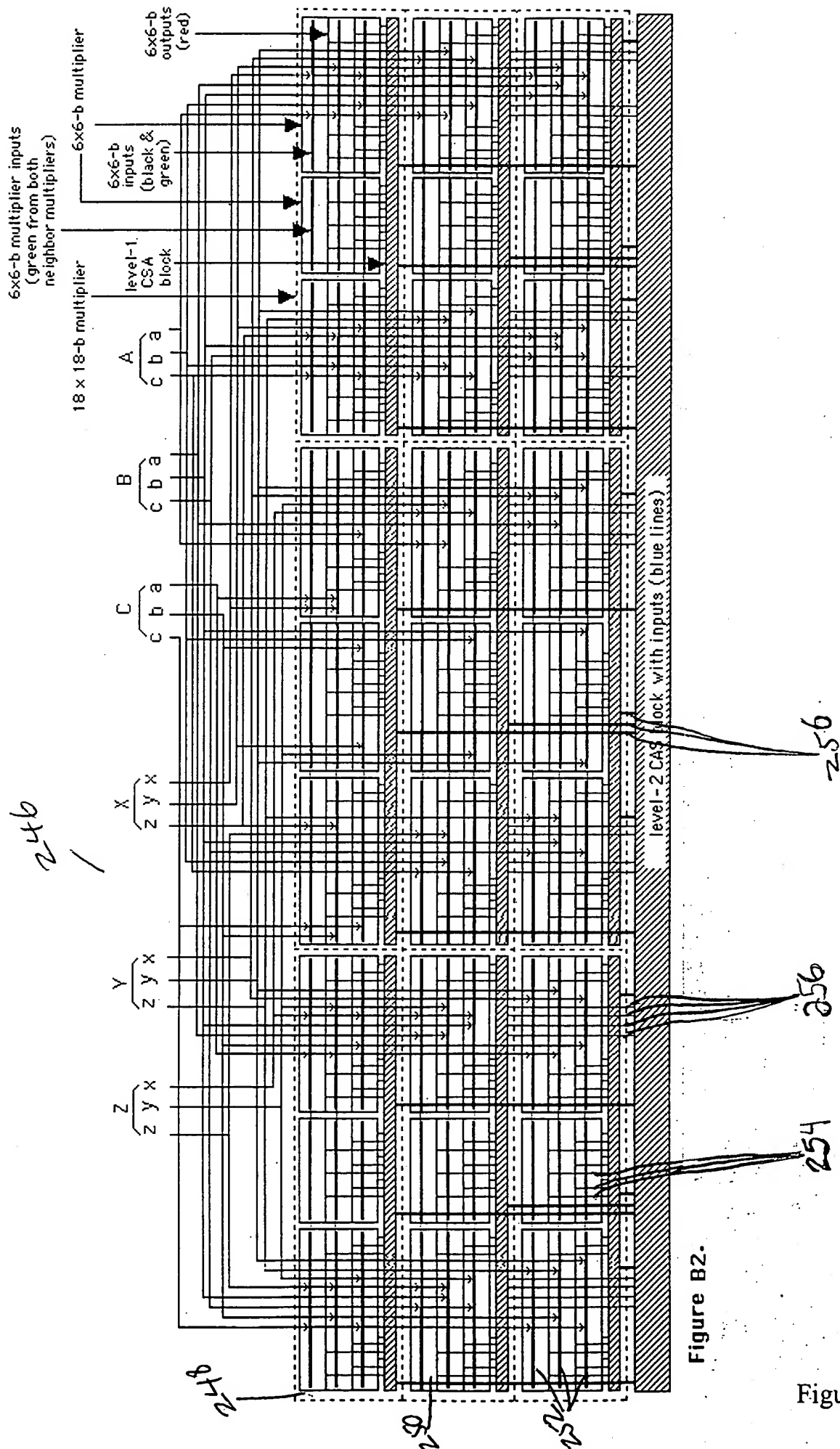


Figure B2.

Figure 10B2



260

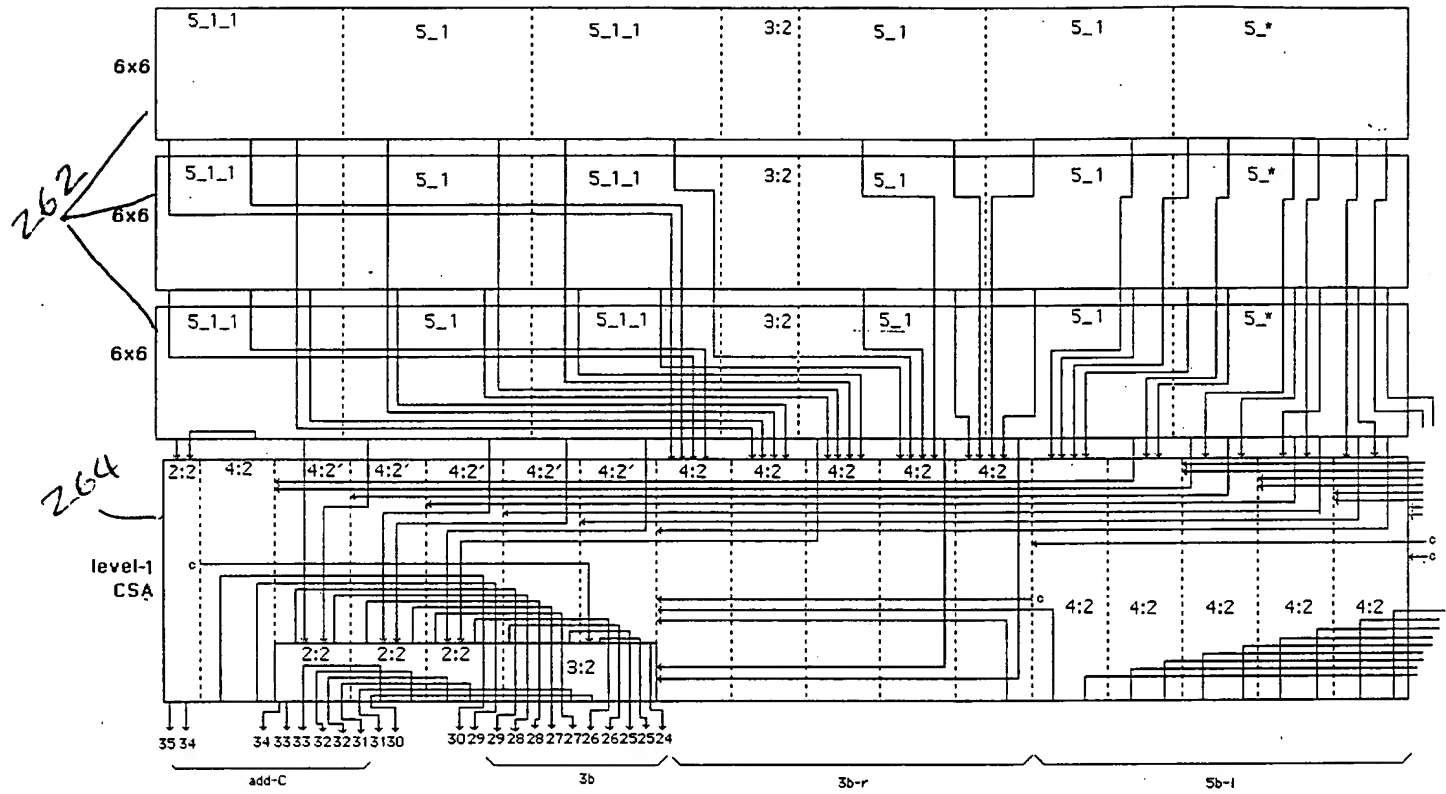


Figure 10B5.

270

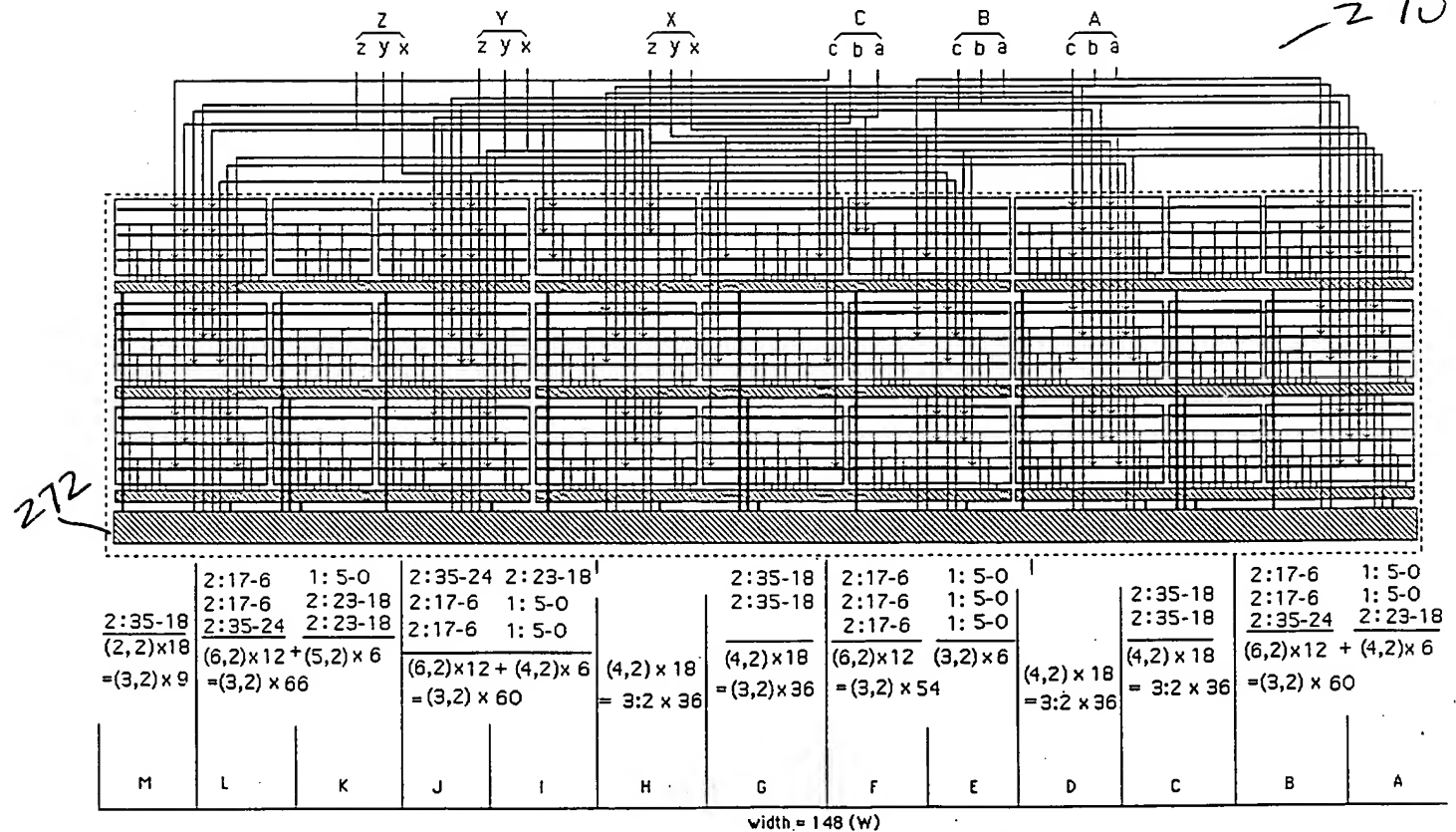


Figure 10B6.

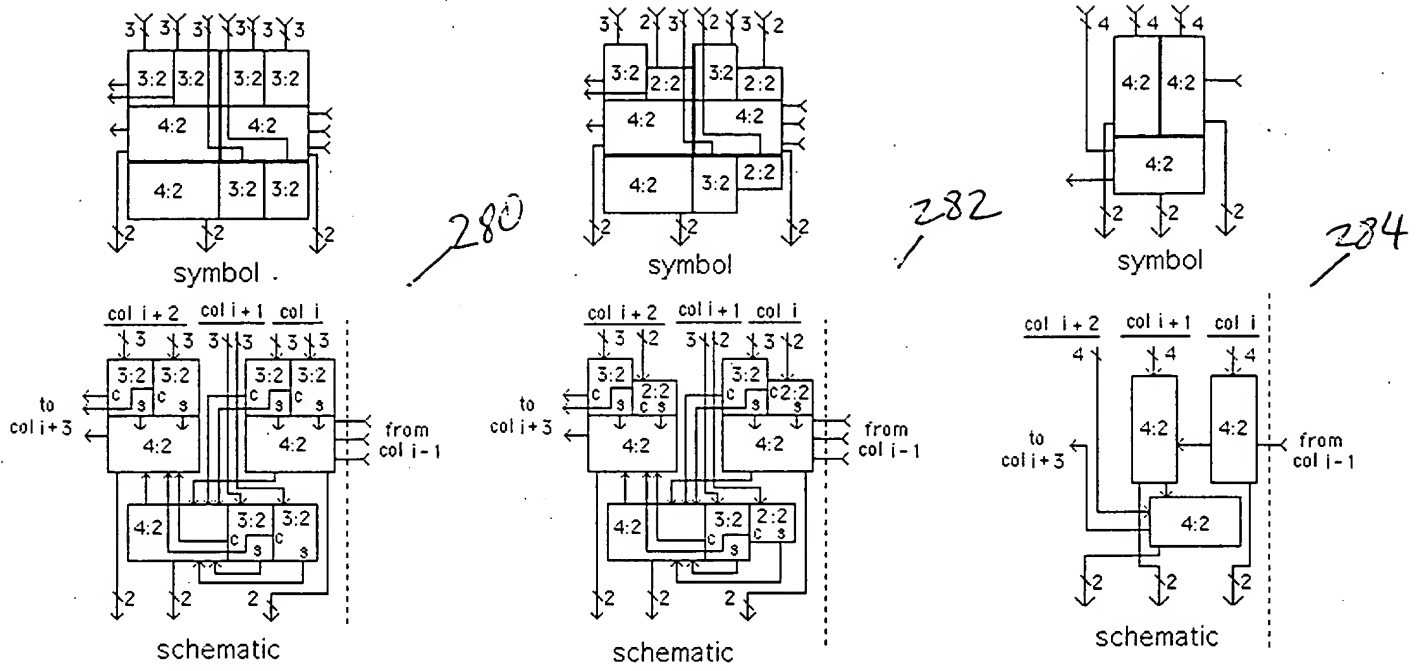


Figure 10B7.

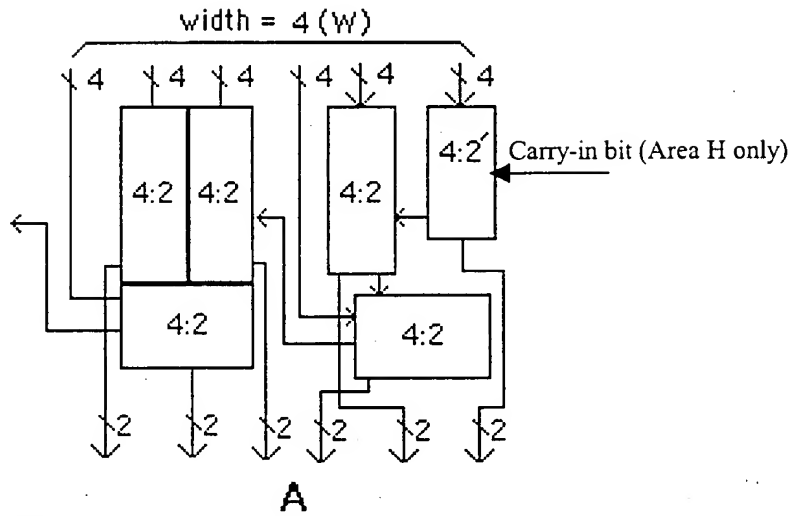


Figure 10B8.

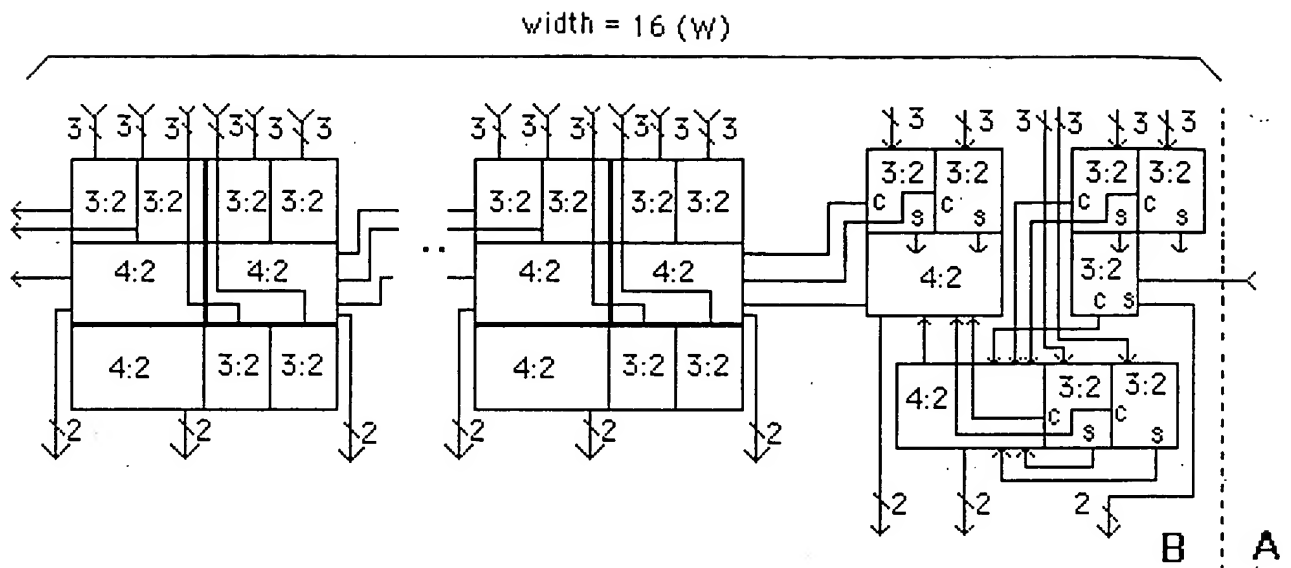


Figure 10B9.

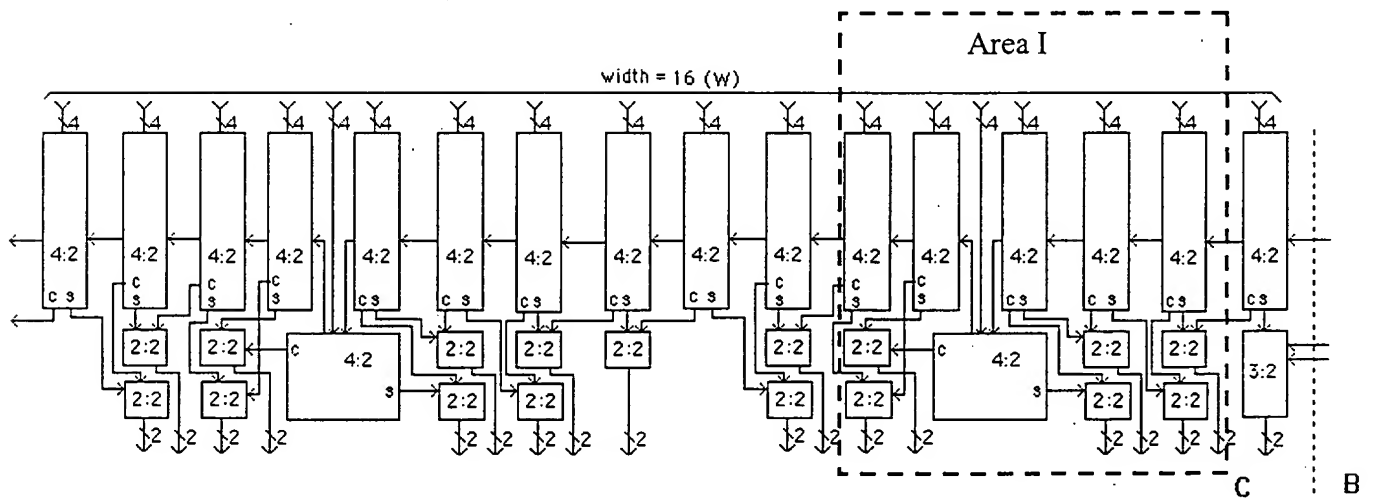


Figure 10B10.

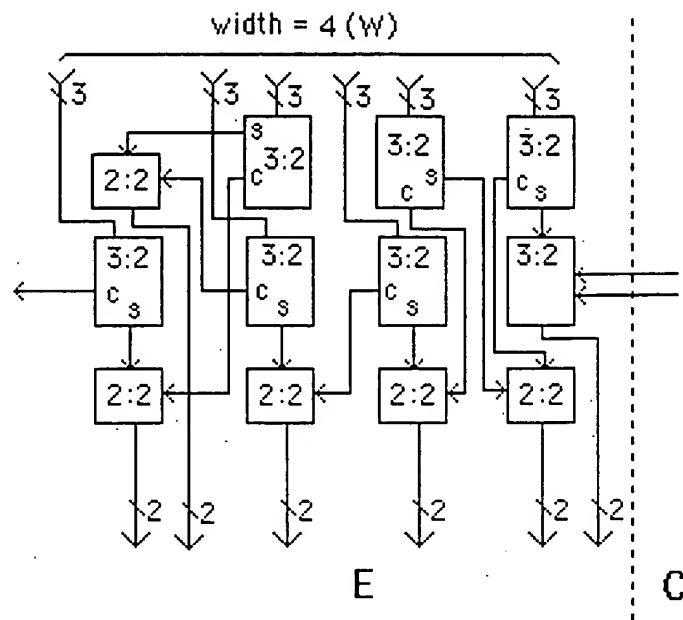


Figure 10B11.

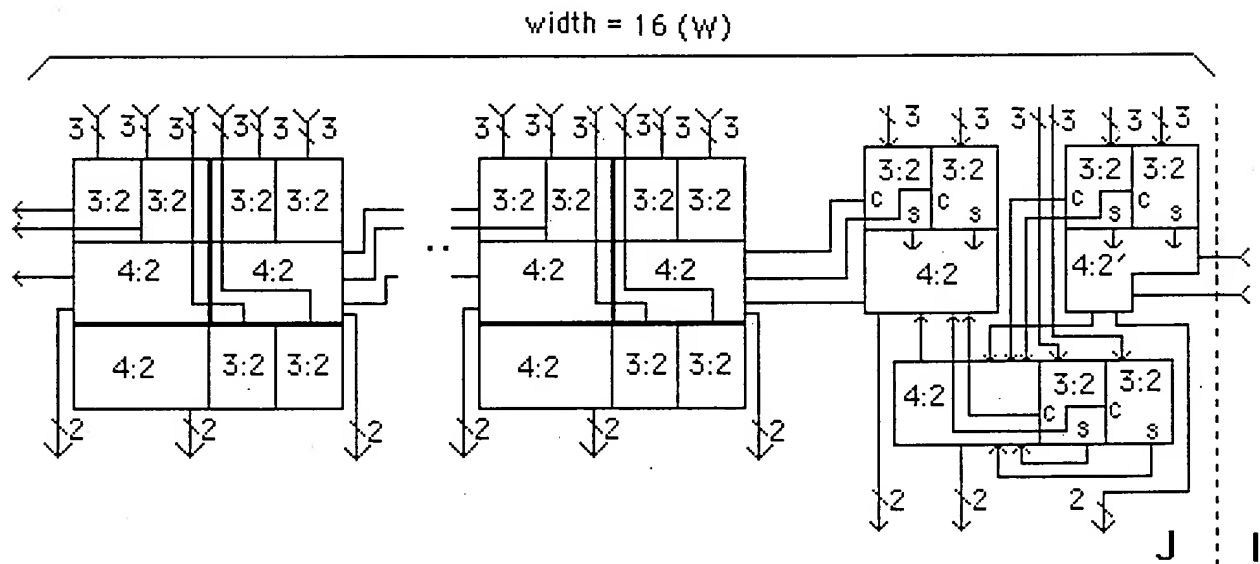


Figure 10B12.



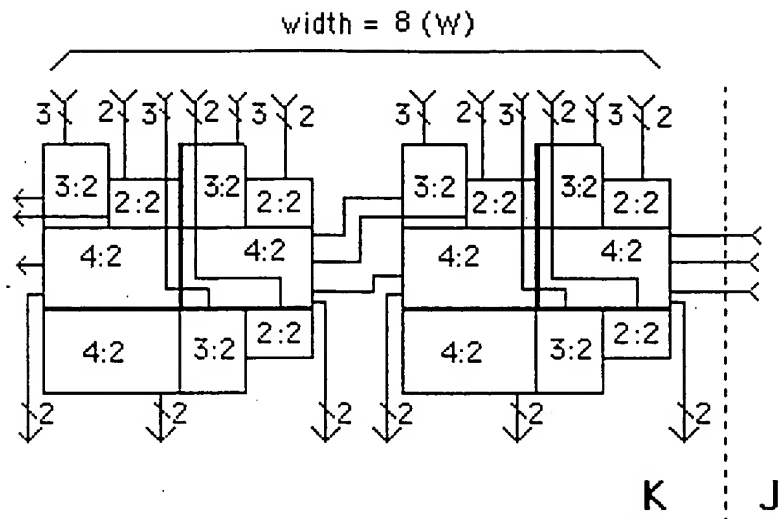


Figure 10B13.

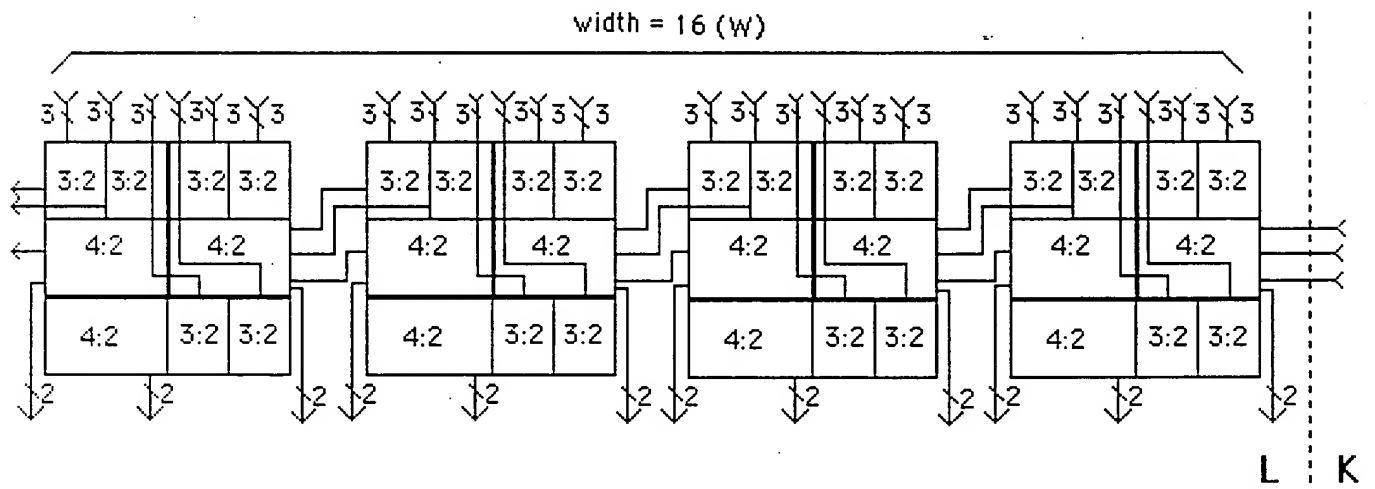


Figure 10B14. The layout draft for area L.

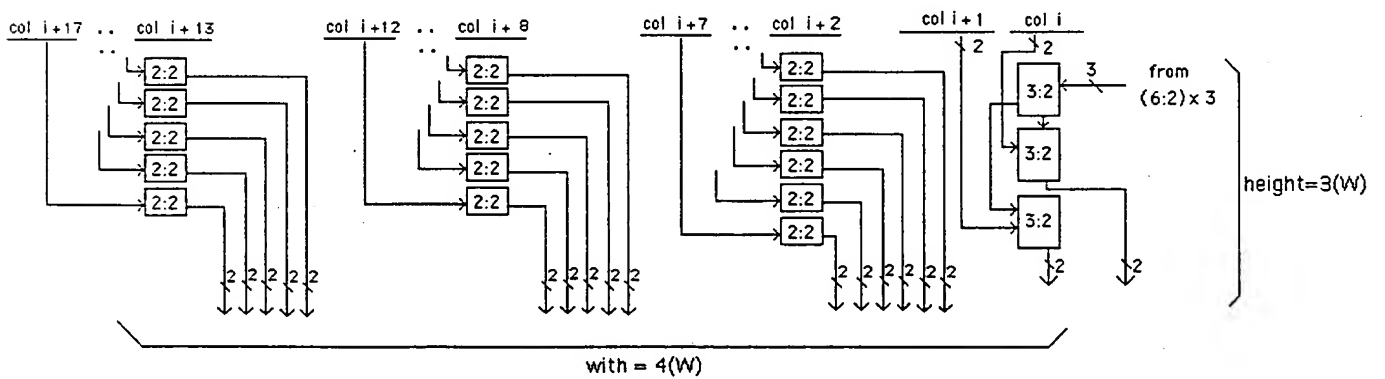


Figure 10B15.

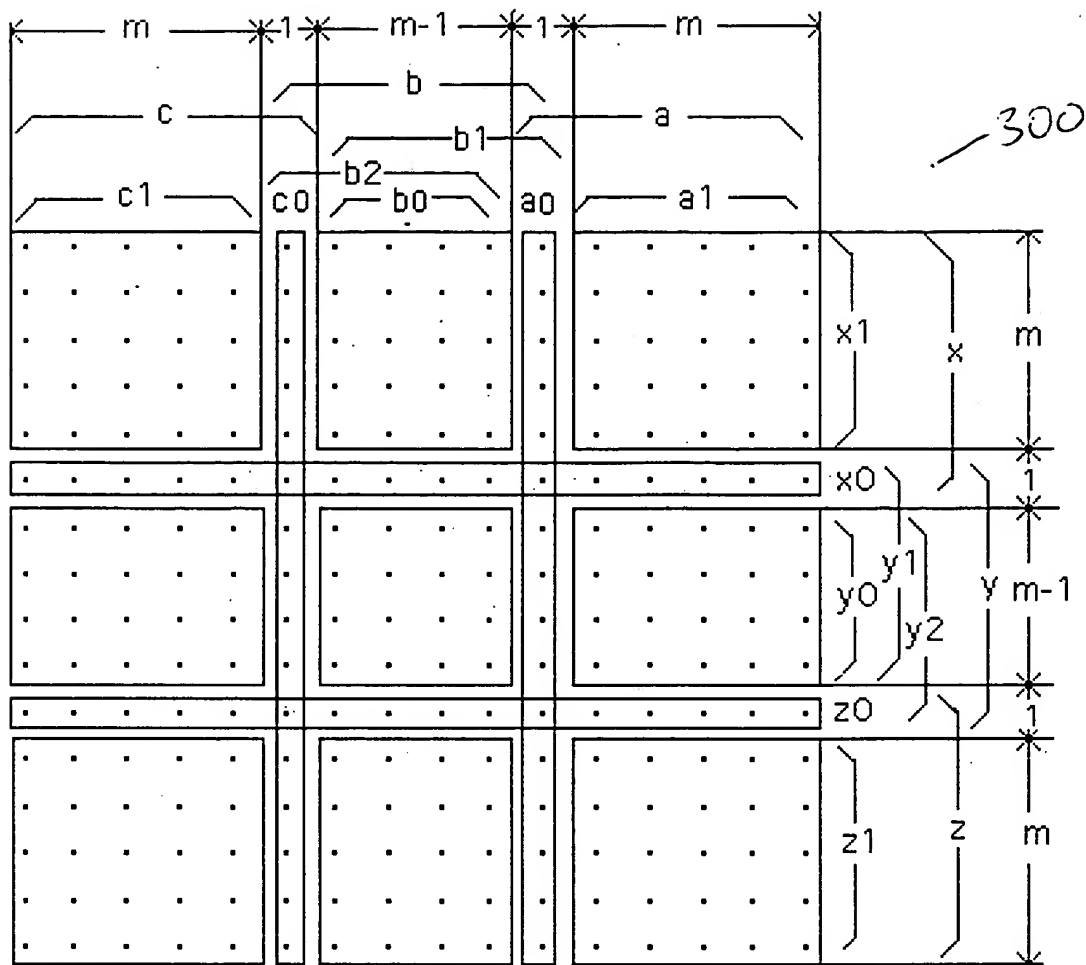


Figure 11A.

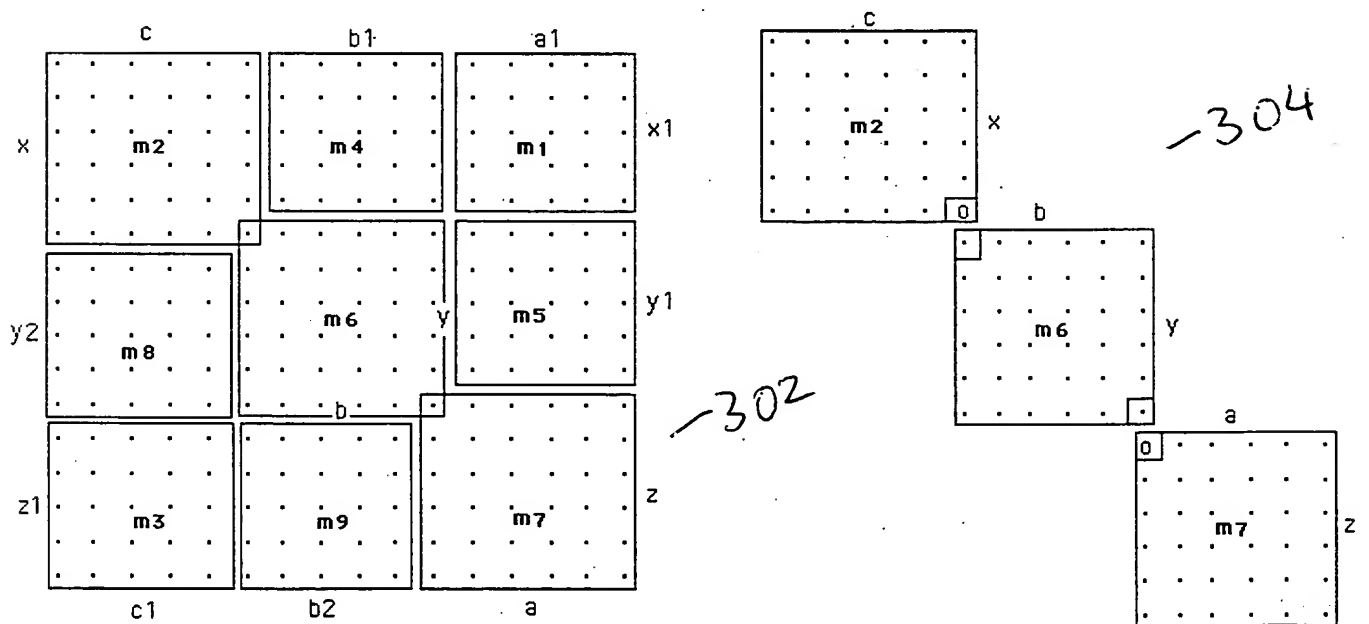


Figure 11B.

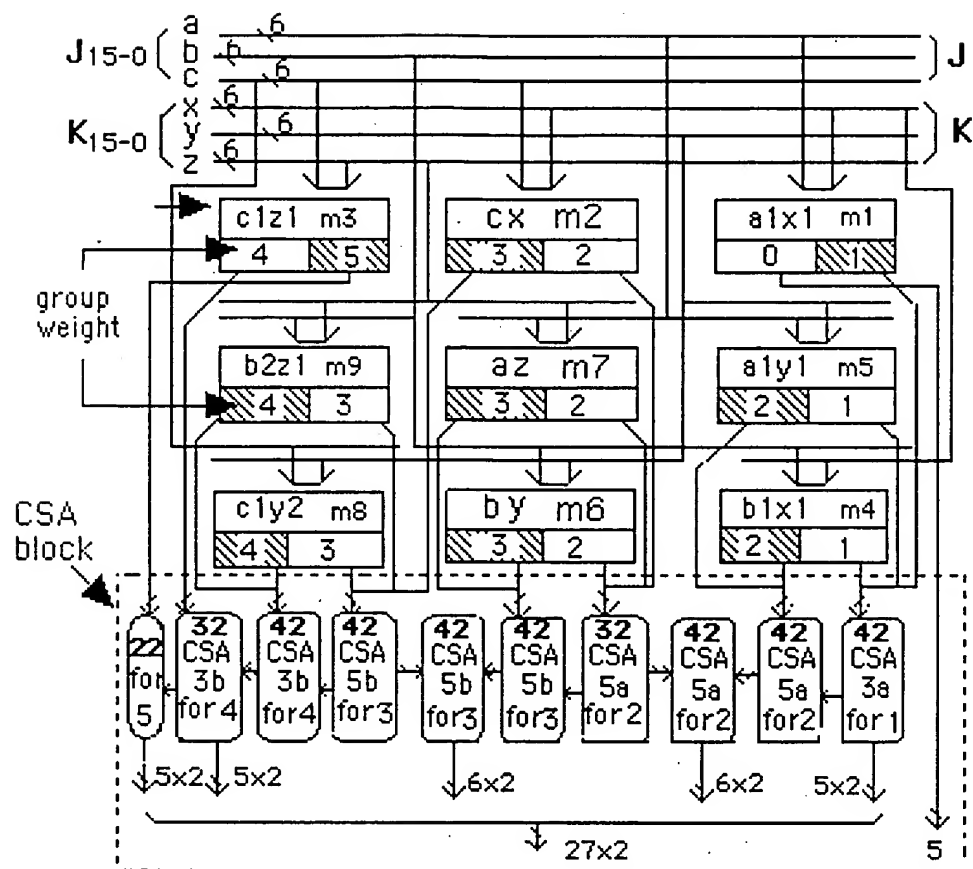


Figure 11C.

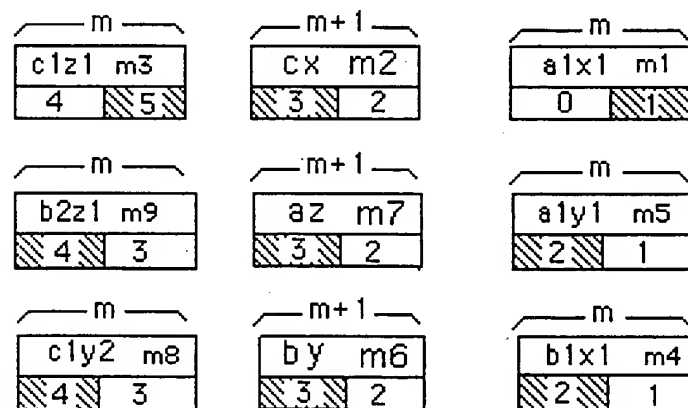


Figure 11D.

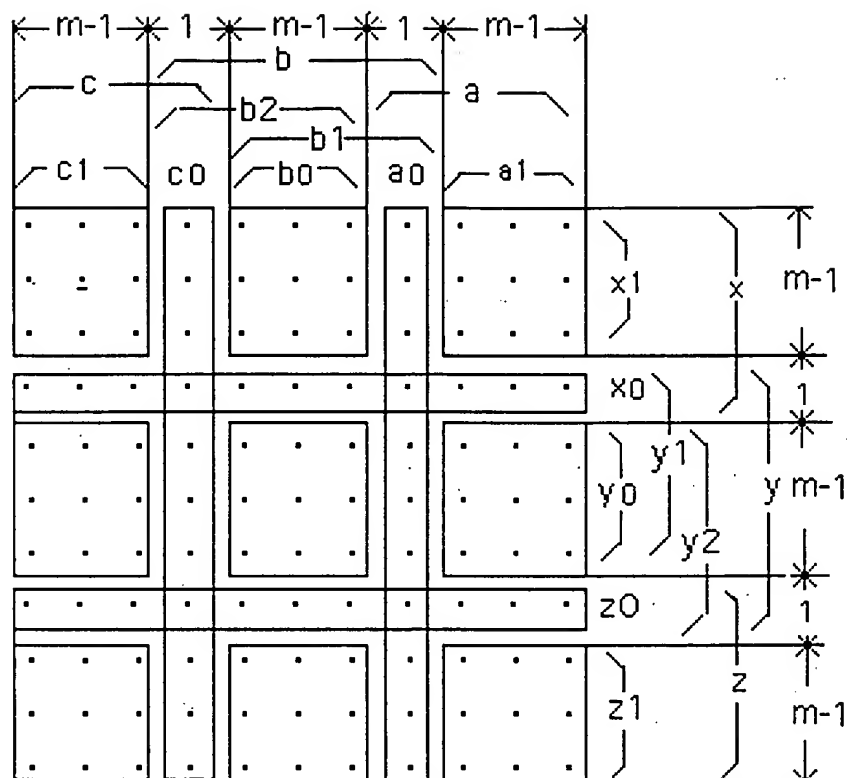


Figure 12A.

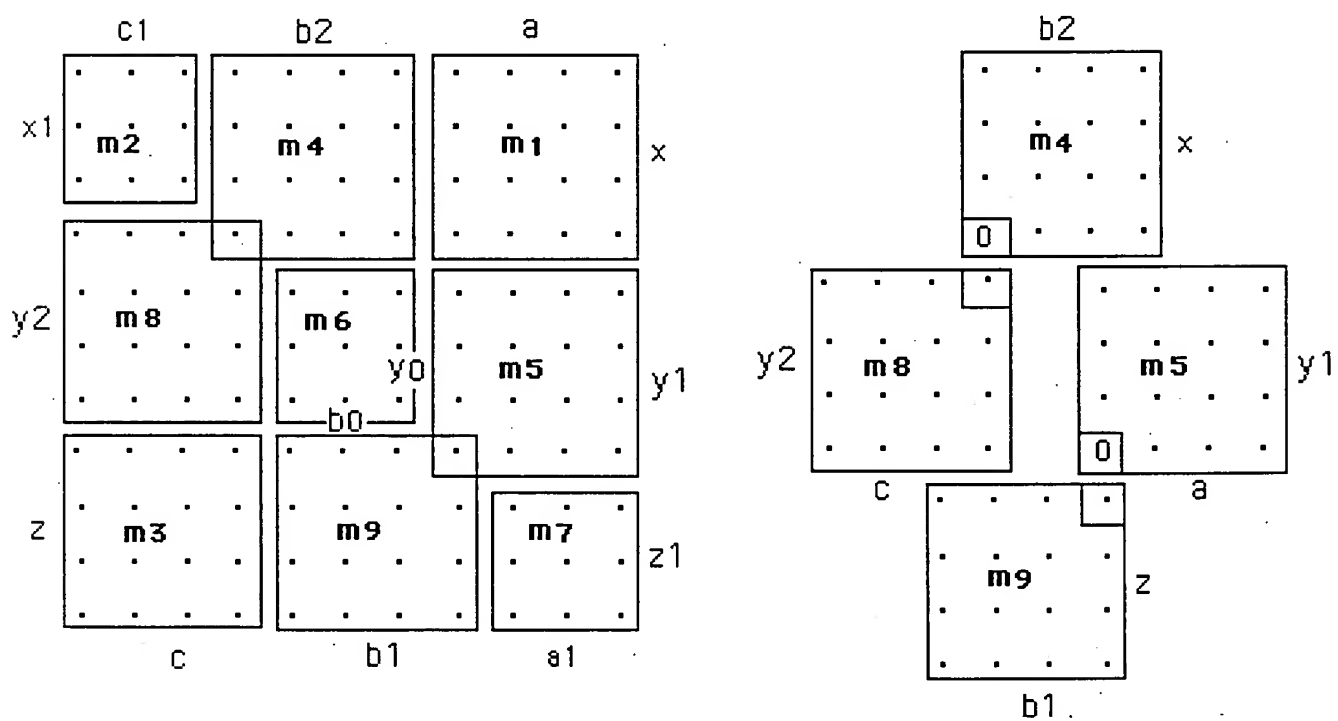


Figure 12B.

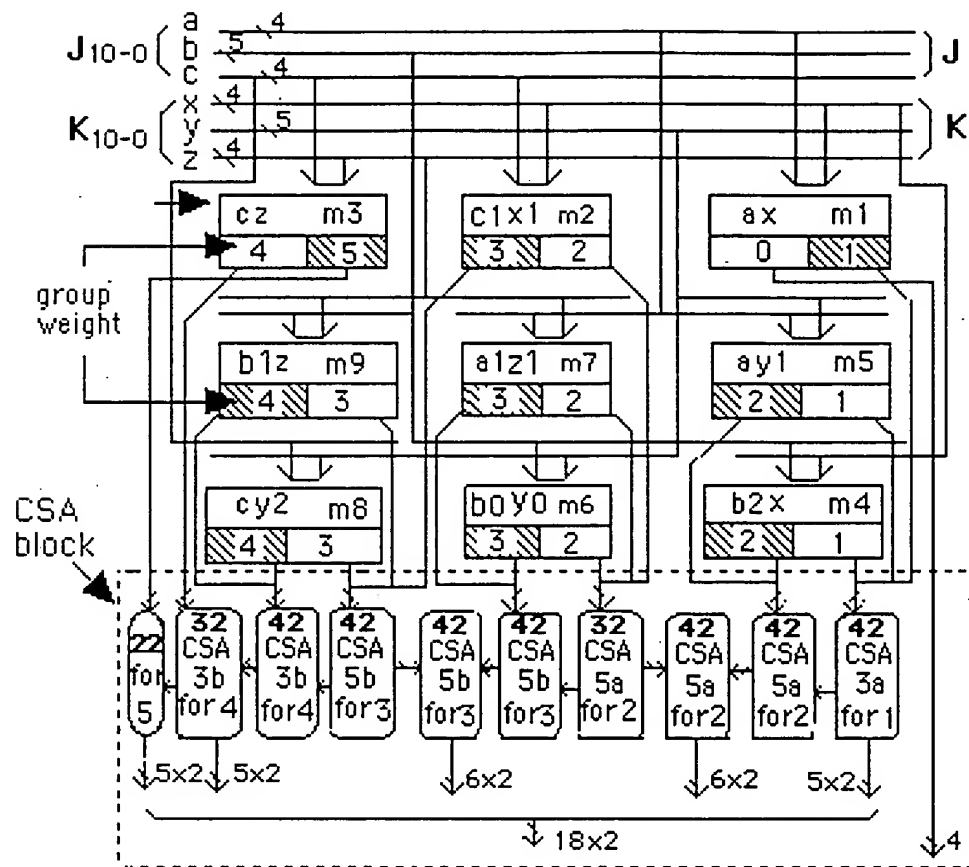


Figure 12C.

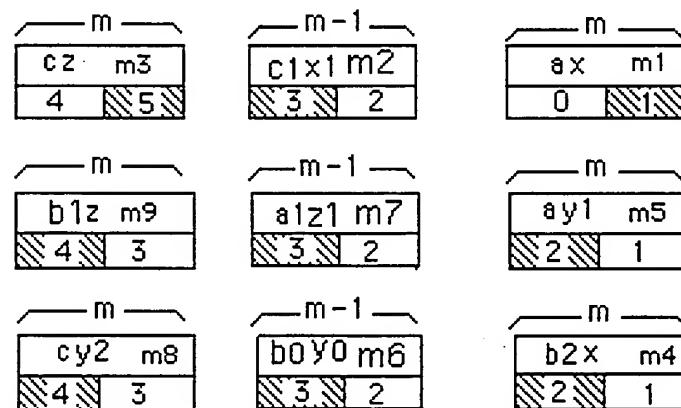


Figure 12D.

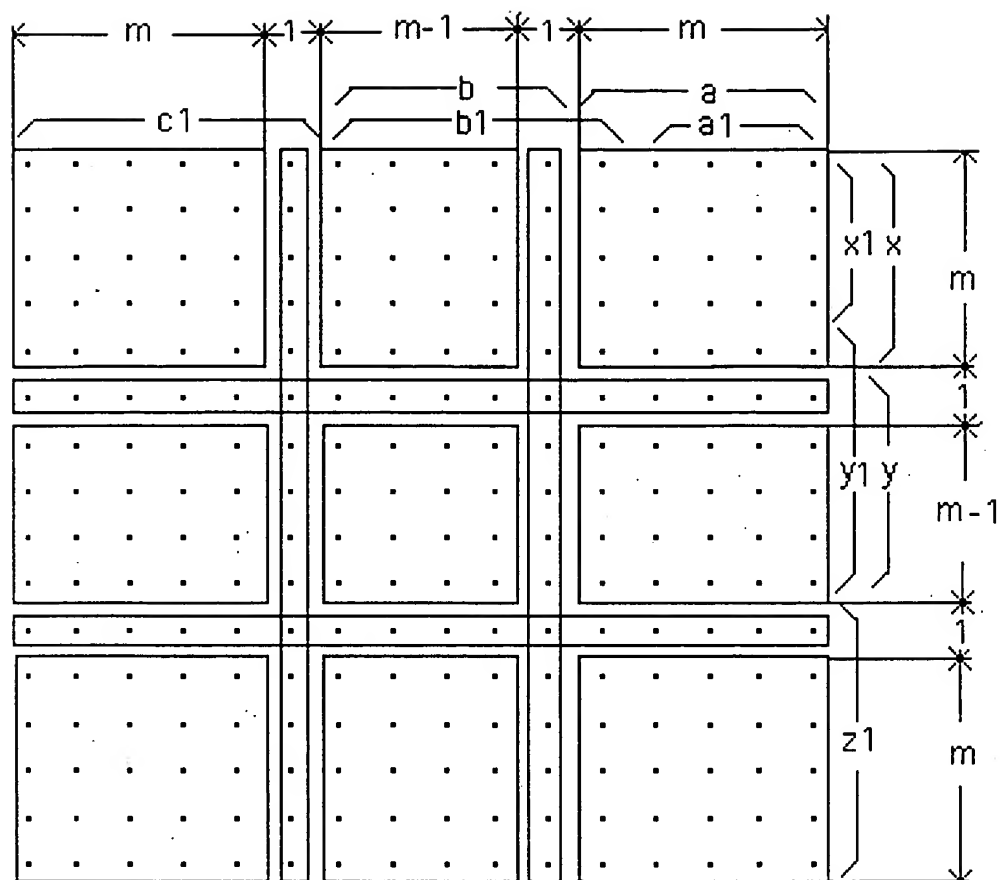


Figure 13A.

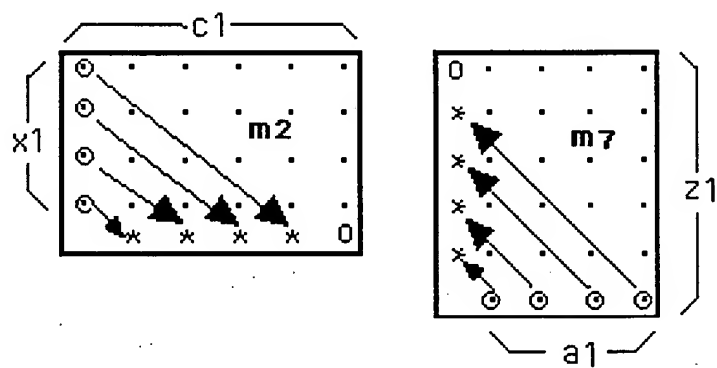
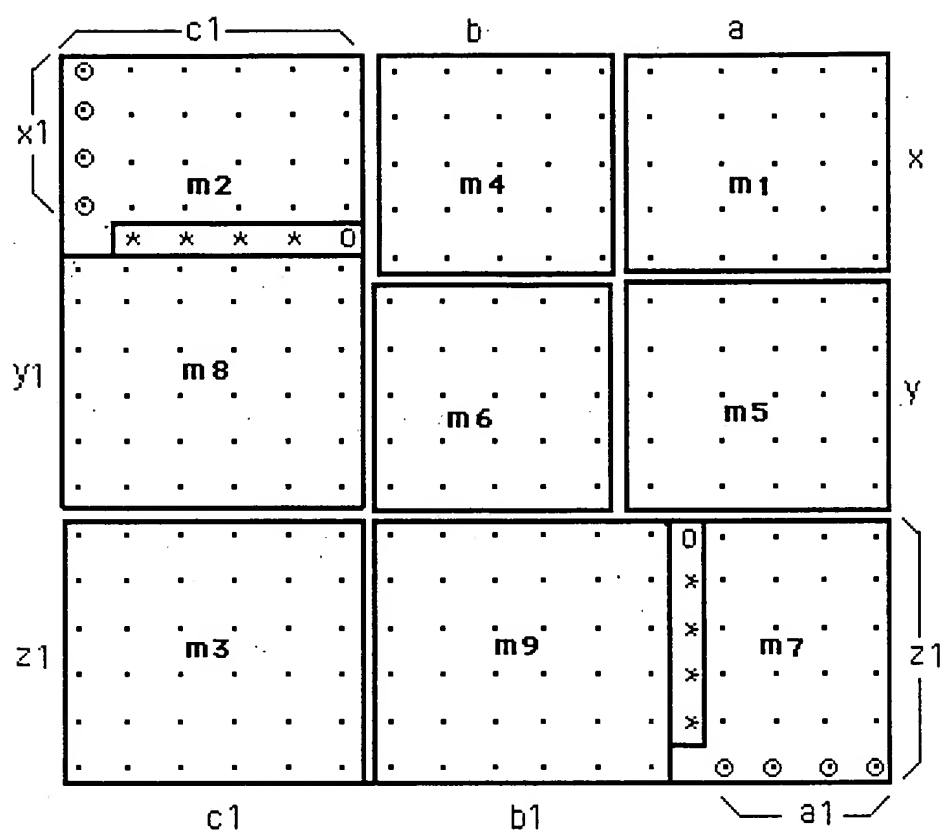


Figure 13B.

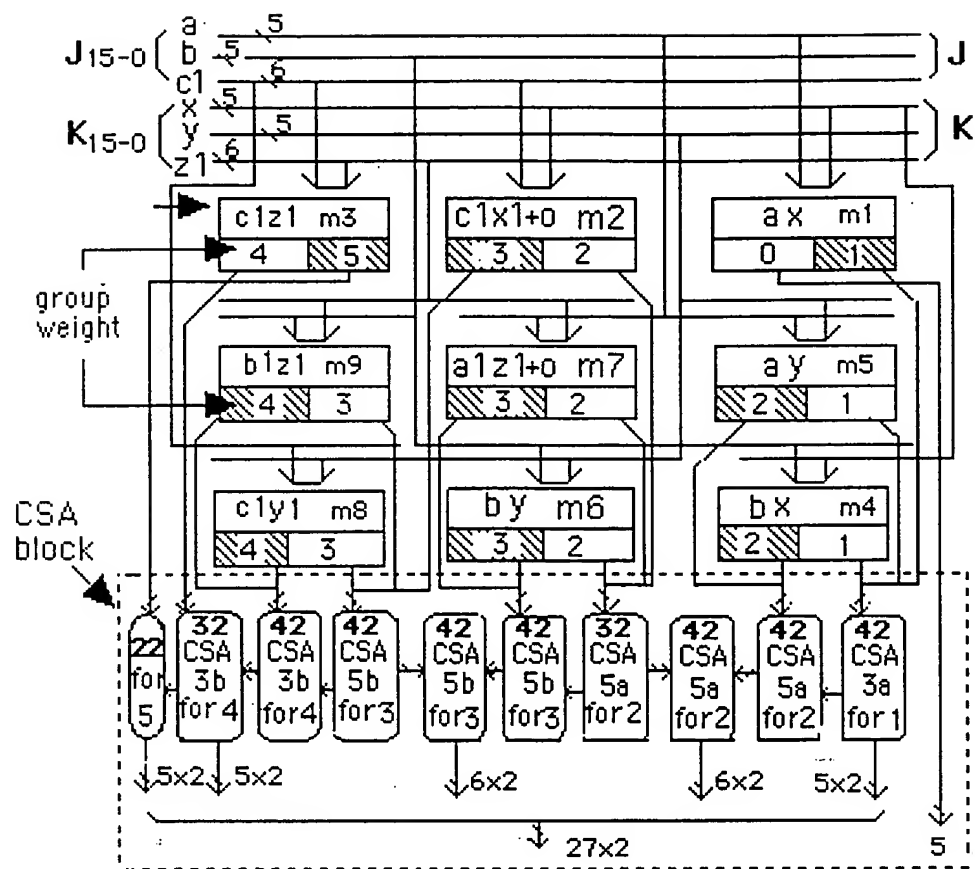


Figure 13C.



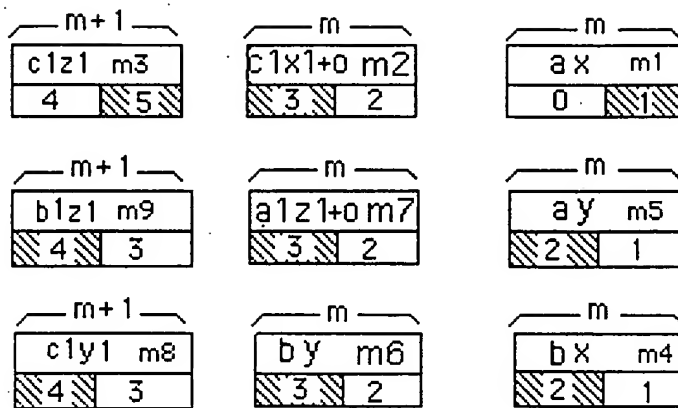


Figure 13D.

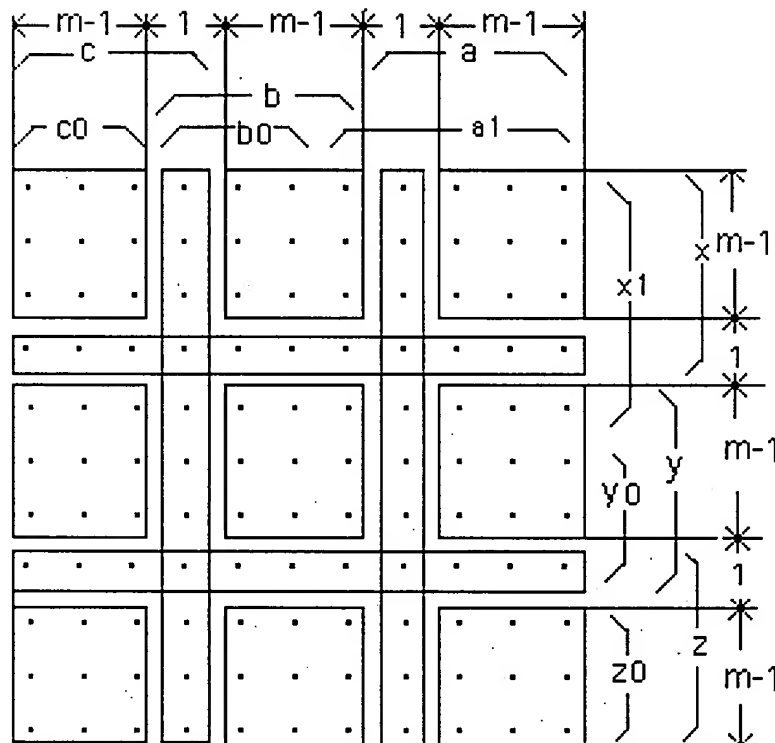


Figure 14A.

340

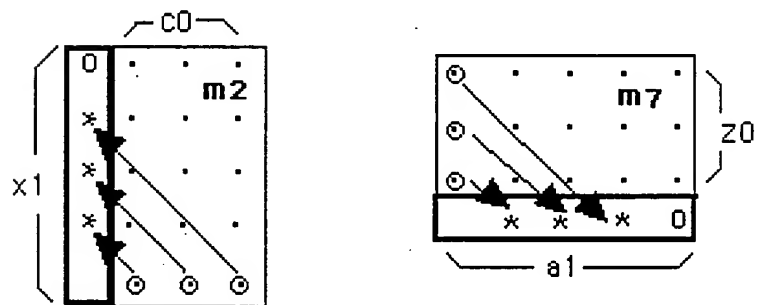
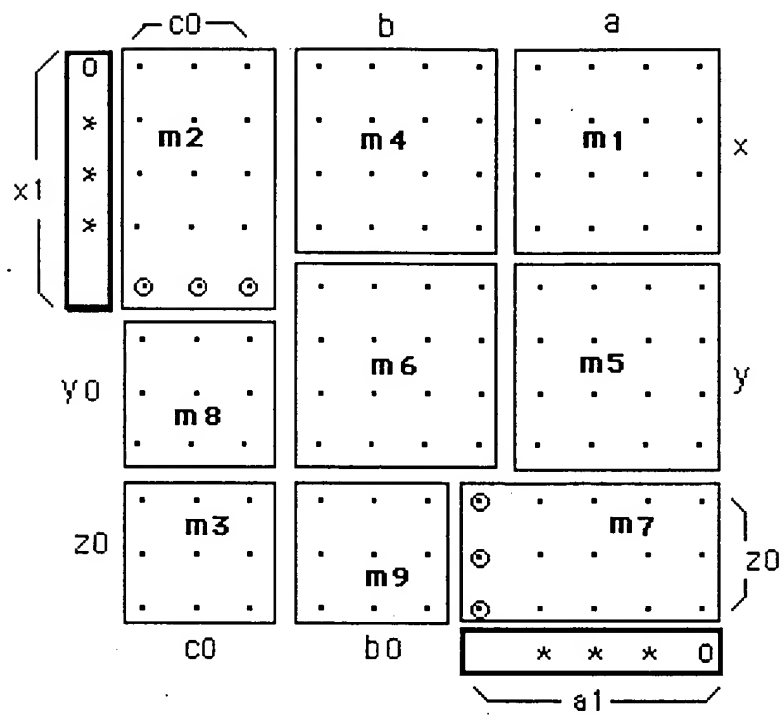


Figure 14B.

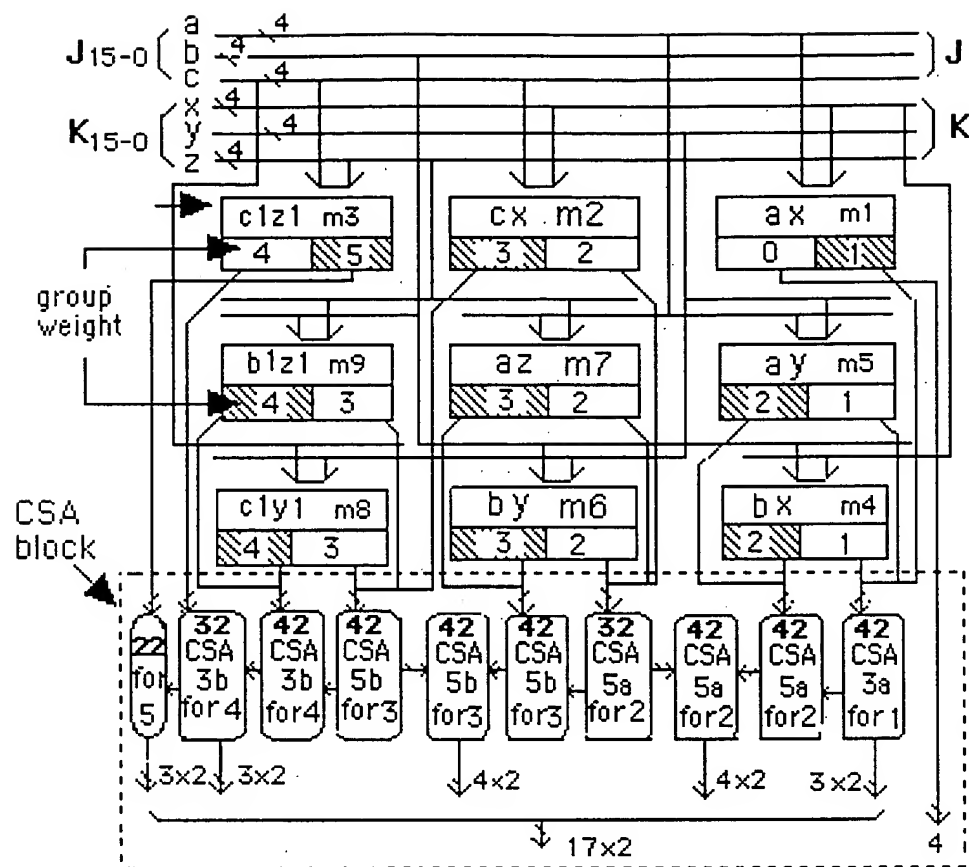


Figure 14C.

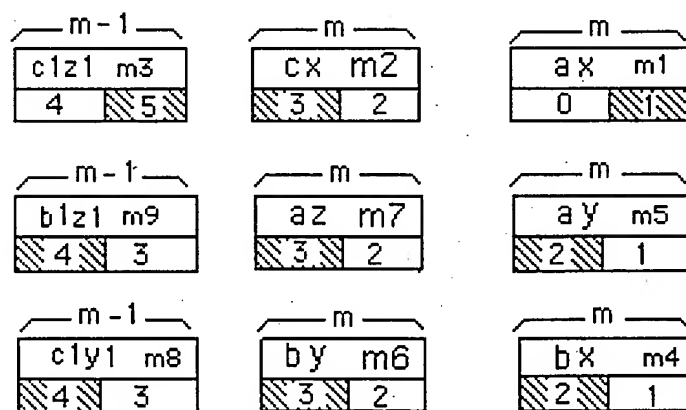


Figure 14D.